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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (Not for submission under 37 CFR 1.99)	Application Number		10715459
	Filing Date		2003-11-19
	First Named Inventor	Curtis Davis	
	Art Unit	2183	
	Examiner Name	Keith E. Vicary	
	Attorney Docket Number	NVDA/AG-08-0112-US	

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1	SUZUAZU et al. "A Microprocessor with a 128-Bit CPU, Ten Floating MAC's, Four Floating-Point Dividers, and an MPEG-2 Decoder," IEEE Journal of Solid State Circuits, Vol. 34, November 1999, pp. 1608-1618.	<input type="checkbox"/>
2	Office Action, Ser. No. 10/715,400, dated February 16, 2010.	<input type="checkbox"/>

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